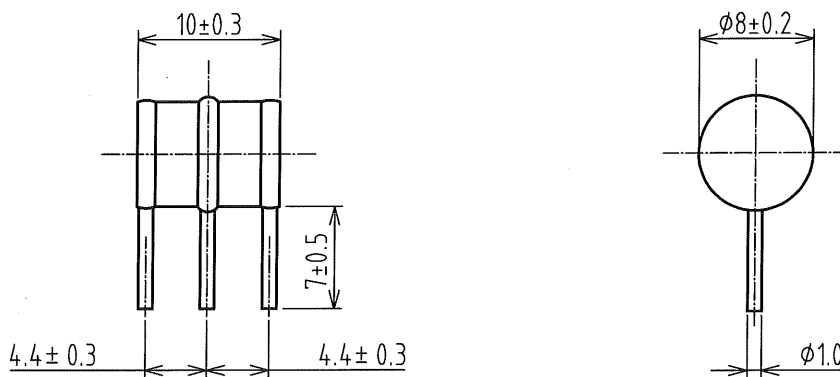
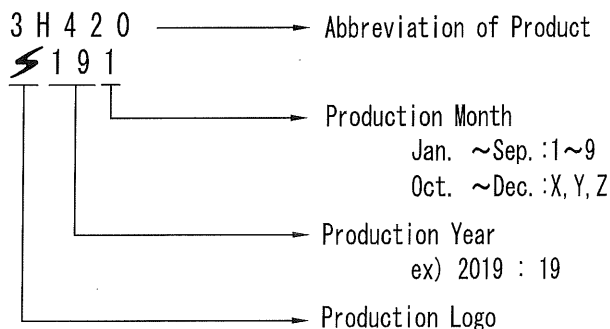


1. Construction and dimensions



※Surface of Lead Wire : Tin plating

2. Marking



1. DC Spark-over Voltage	100V/s	420V ± 20%
2. Impulse Spark-over Voltage	1kV/μs	≤ 1000V
3. Insulation Resistance	100V DC	≥ 10,000MΩ
4. Capacitance	1MHz	≤ 3.0pF
5. DC Holdover Voltage	*DC135V ITU-T K. 12	≤ 150ms
6. Impulse Life	10/1000 μs, 100A × 2	300times
7. Impulse Discharge Current	8/20 μs 5kA × 2	+5, -5times
8. AC Discharge Current	50Hz 1sec, 5A × 2	5times

After Test of Item 6 and 7 and 8

1) DC Spark-over Voltage	100V/s	300~550V
2) Impulse Spark-over Voltage	1kV/μs	≤ 1100V
3) Insulation Resistance	100V DC	≥ 100MΩ

*Test circuit shall comply with ITU-T K.12/Fig.5 and added R4,C2.

DSN	S.E.D	Jan.18.'19	UNIT	mm	A4	TITLE	CERAMIC ARRESTER
DWG	<i>Y. Mima</i>	Jan.18.'19	SCALE	2/1			3H-420J1
CHK	<i>H. Takahashi</i>	Jan.18.'19				DWG No.	T-180580C01
							REV.